

TITLE: SEMICONDUCTOR BGA PACKAGE HAVING A
SEGMENTED VOLTAGE PLANE AND METHOD OF MAKING
Inventor: Morrison et al.
Serial No.: To Be Assigned
Docket No.: 2269-6103US

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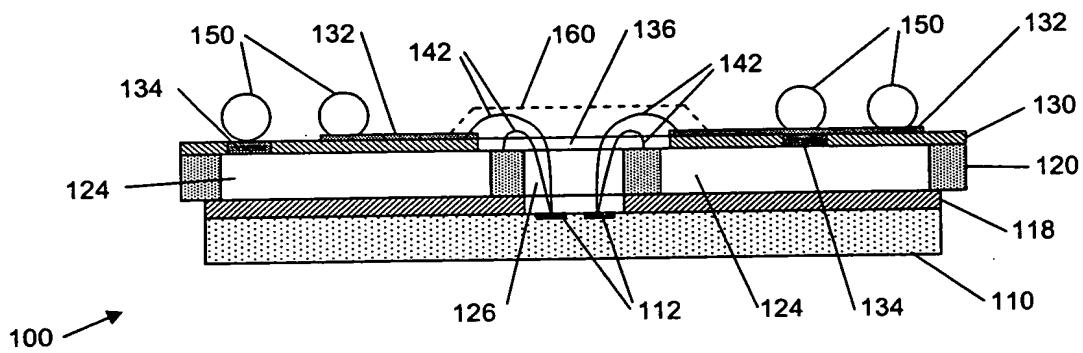


FIG. 1

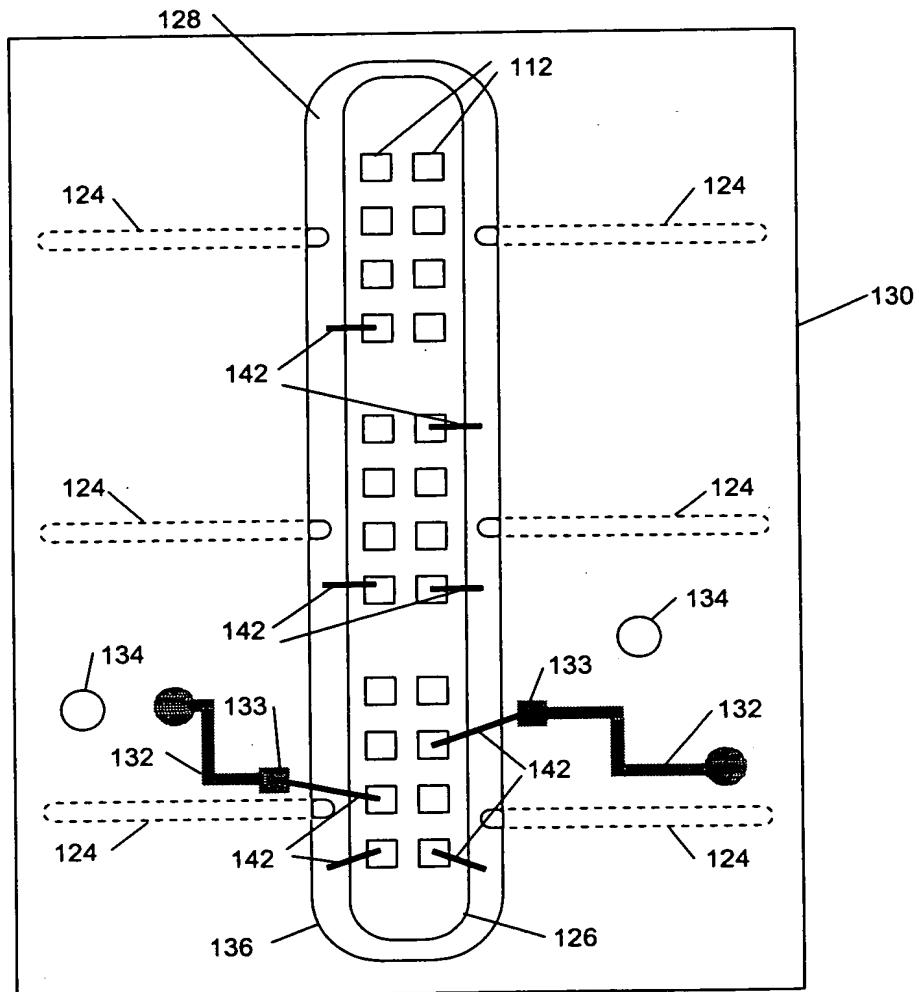


FIG. 2

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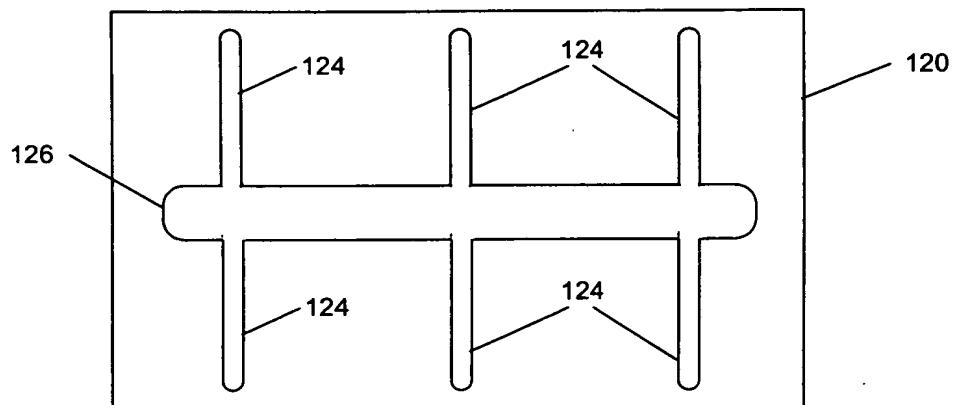


FIG. 3A

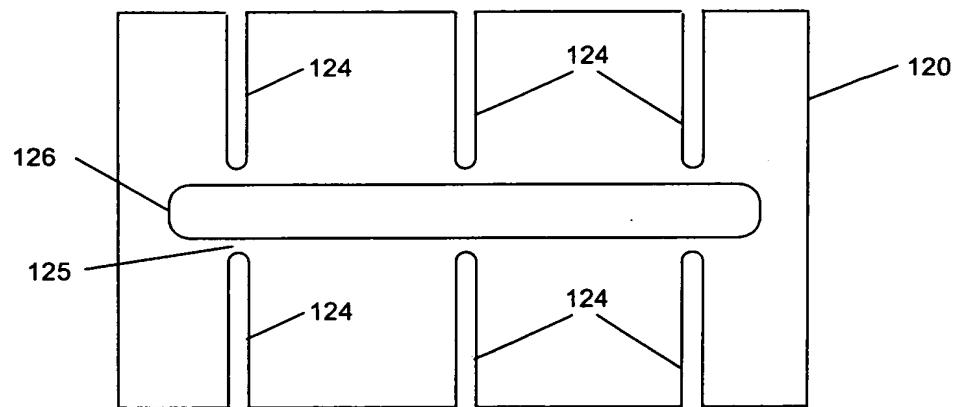


FIG. 3B

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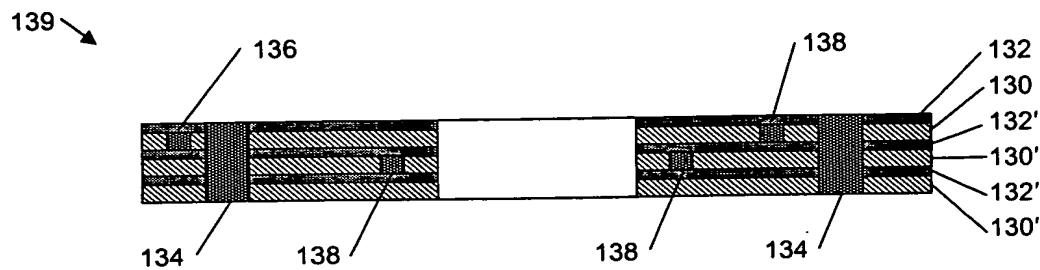


FIG. 4

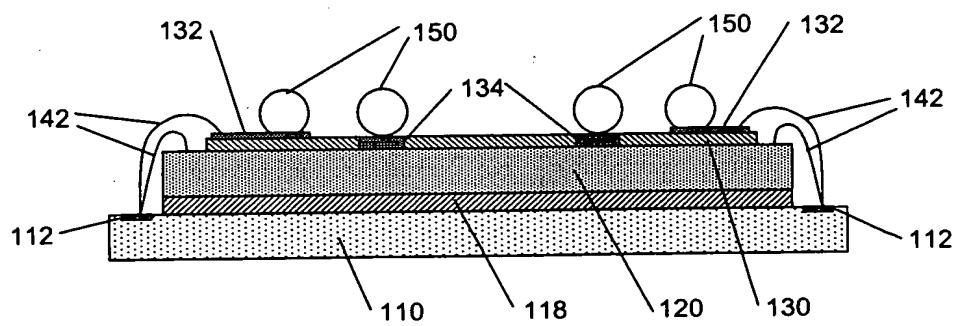


FIG. 5

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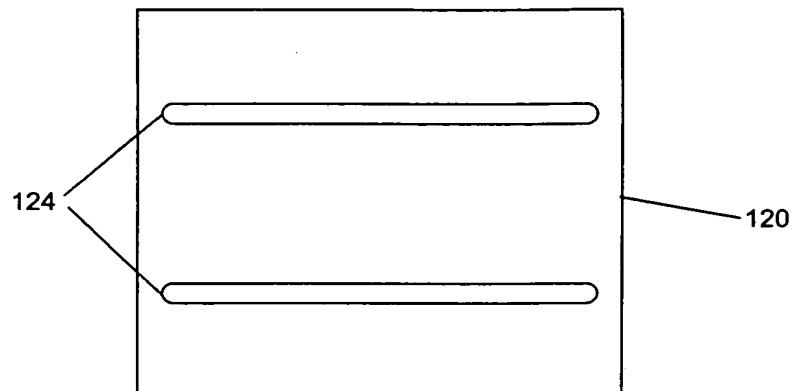


FIG. 6A

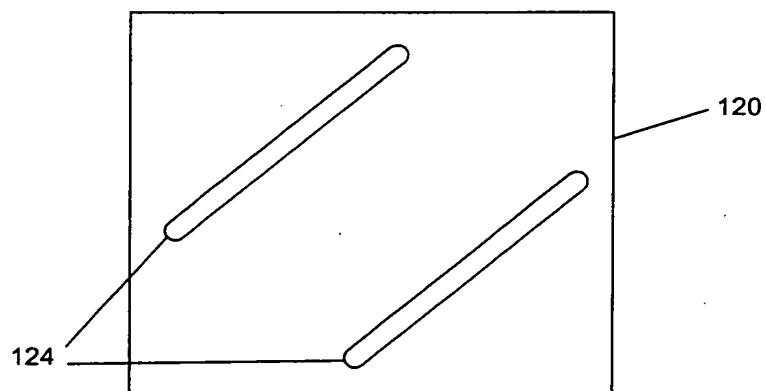


FIG. 6B

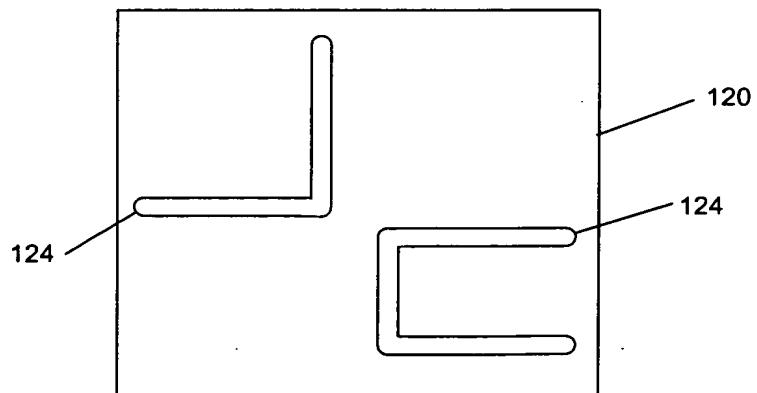


FIG. 6C